# Wideband, Microwave

# **Monolithic Amplifier**

**AVA-183A+** 

 $50\Omega$ 5 to 18 GHz

# The Big Deal

- Surface Mount Amplifier up to 18 GHz
- · Integrated matching, DC Blocks and bias circuits
- Superior Value
- Suitable for low phase noise applications



CASE STYLE: DQ849

# **Product Overview**

The AVA-183A+ is a surface mount, microwave amplifier fabricated using InGaAs PHEMT technology and is a fully integrated gain block up to 18 GHz. It is packaged in Mini-Circuits industry standard 3x3 mm MCLP package, which provides excellent RF and thermal performance. The AVA-183A+ integrates the entire matching network with the majority of the bias circuit inside the package, reducing the need for complicated external circuits. This approach makes the AVA-183A+ extremely flexible and enables simple, straightforward use.

# **Key Features**

Feature	Advantages
Wideband, 5 GHz to 18 GHz	Broad frequency range supports a wide array of applications from microwave radio and radar , to military communications and countermeasures.
Excellent Gain Flatness	Typical ±1.2 dB gain flatness across the entire frequency range minimizes the need for external equalizer networks making it a great fit for instrumentation and EW applications.
High Isolation	With reverse isolation of 31-42 dB (17-23 dB directivity), the AVA-183A+ is an excellent choice for buffering broadband circuits. It is an ideal LO driver amplifier and provides designers system flexibility and margin when integrating cascaded RF components.
Single +5V Supply	No hassle associated with amplifiers using dual supply; such as power supply sequencing.     Integrated output bias-tee, simplifies layout & reduces cost.
Manufacturability	MSL1 and ESD Class1A (HBM) ratings minimize special handling on production lines.
Low additive phase noise, typically -151 dBc/Hz @10 KHz offset	Ideal for low phase noise synthesizer applications

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# Wideband, Microwave

# Monolithic Amplifier 5 GHz - 18 GHz

#### **Product Features**

- Gain, 13.8 dB typ. & Flatness, ±1.2 dB
- Output Power, up to +19.0 dBm typ.
- Excellent isolation, 36 dB typ. at 12 GHz
- Single Positive Supply Voltage, 5.0V
- Integrated DC blocks, Bias-Tee & Microwave bypass capacitor
- Unconditionally Stable
- Aqueous washable; 3mm x 3mm SMT package



+RoHS Compliant
The +Suffix identifies RoHS Compliance. See our web site

for RoHS Compliance methodologies and qualifications

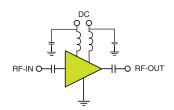
## **Typical Applications**

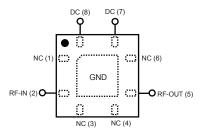
- Military EW and Radar
- DBS
- Wideband Isolation amplifier
- Microwave point-to-point radios
- Satellite systems

## **General Description**

The AVA-183A+ is a wideband monolithic amplifier fabricated using InGaAs PHEMT technology with outstanding gain flatness up to 18 GHz. It is unconditionally stable, outstanding isolation enables it to be used as a wideband isolation amplifier or buffer amplifier in a variety of microwave systems.

#### simplified schematic and pad description





Function	Pad Number	Description (See Application Circuit, Fig. 2)
RF-IN	2	RF input pad
RF-OUT	5	RF output pad
DC	8(V <sub>D1</sub> ), 7(V <sub>D2</sub> )	DC power supply
GND	paddle in center of bottom	Connected to ground
NOT USED	1,3,4,6	No internal connection; recommended use: per PCB Layout PL-328

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## Electrical Specifications<sup>(1)</sup> at 25°C, Zo=50 $\Omega$ , (refer to characterization circuit, Fig. 1)

Parameter	Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		5.0		18.0	GHz
DC Voltage (V <sub>D1</sub> , V <sub>D2</sub> )			5.0		V
DC Current (I <sub>D1</sub> +I <sub>D2</sub> )		104	131	166	mA
(/	5.0	_	12.9	_	
	8.0	12.0	14.7	<u> </u>	
	10.0	12.0	14.0	_	
Gain	12.0	_	13.4	_	dB
	14.0	_	13.1	_	
	16.0	_	13.6	_	
	18.0	10.8	12.4	_	
	5.0		10.4		<del>                                     </del>
	8.0		17.0		
	10.0		11.0		
Input Return Loss	12.0		11.0		dB
mpat Hotam 2000	14.0		11.0		GD
	16.0		11.0		
	18.0		7.5		
	5.0		8.0		
	8.0		18.0		
	10.0		14.0		dB
Output Return Loss	12.0		11.0		
Output Hetuin Loss	14.0		10.6		
	16.0		11.2		
	I		11.8		
	18.0 5.0		32.2		
			29.0		
	8.0		1		
Output IDO (2)	10.0		27.7		dD.co
Output IP3 (2)	12.0		26.3		dBm
	14.0		25.1		
	16.0		24.3		
	18.0		24.4		
	5.0	_	17.6		
	8.0	_	18.0		
0	10.0	16.0	19.0		I.D.
Output Power @ 1 dB compression	12.0	_	19.0		dBm
	14.0	_	19.9		
	16.0	_	19.6		
	18.0	_	18.7		
	5.0		7.4		
	8.0		4.3		
	10.0		4.5		
Noise Figure	12.0		4.8		dB
	14.0		5.1		
	16.0		5.1		
	18.0		6.0		
Additive Phase Noise	5.0 GHz, 10KHz offset		-151		dBc/Hz
Directivity (Isolation-Gain)	12		23.1		dB
DC Current Variation vs. Temperature (3)			0.046		mA/°C
Thermal Resistance			61		°C/W

#### Absolute Maximum Ratings(4)

Absolute Maximum matings				
Parameter	Ratings			
Operating Temperature (5)	-40°C to 85°C			
Storage Temperature	-55°C to 100°C			
Channel Temperature	150°C			
DC Voltage (Pad 7,8)	5.5 V			
DC Voltage (Pads 2, 5)	10 V			
Power Dissipation	980 mW			
DC Current (Pad 7+8)	180 mA			
Input Power (CW)	20 dBm			



110-115

120-125

135-140 140-145

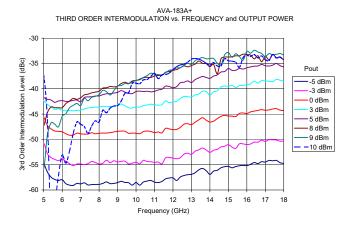
130-135 Current (mA)

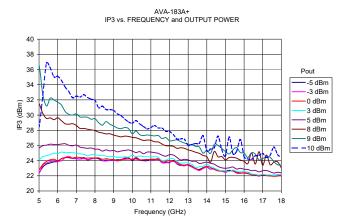
<sup>|</sup> Measured on Mini-Circuits Characterization test fixture TB-547-2+
| See Characterization Test Circuit (Fig. 1)
| At Pout=9dBm/tone
| Current at 85°C - Current at -45°C)/130
| Permanent damage may occur if any of these limits are exceeded.
| These maximum ratings are not intended for continuous normal operation.
| Online with reference to report and next terms.

<sup>(5)</sup> Defined with reference to ground pad temperature.

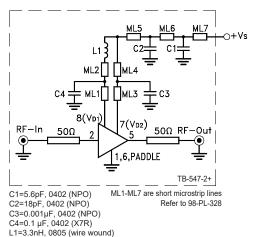
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## Intermodulation and IP3 vs. Frequency and Output Power





#### **Characterization Test Circuit**



#### **Recommended Application Circuit**

(refer to evaluation board for PCB Layout and component values)

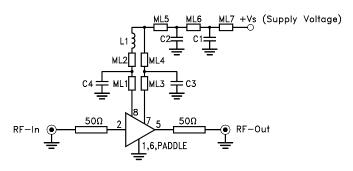


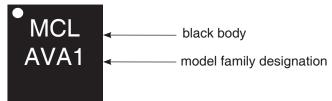
Fig 2. Recommended Application Circuit

**Fig 1.** Block Diagram of Test Circuit used for characterization. (DUT soldered on Mini-Circuits Characterization Test Board TB-547-2+) Gain, Output power at 1dB compression (P1dB), Noise Figure, Output IP3 (OIP3) are measured using Agilent's N5242A PNA-X microwave network analyzer.

#### Conditions:

- 1. Gain: Pin=-25 dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1 MHz apart, 9 dBm/tone at output.
- 3. Vs adjusted for 5.0V at device ( $V_{\rm D1}$  and  $V_{\rm D2}$ ), compensating loss of bias lines.

# **Product Marking**



Marking may contain other features or characters for internal lot control

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Additional Detailed Technical Information additional information is available on our dash board. To access this information click here				
	Data Table			
Performance Data	Swept Graphs			
	S-Parameter (S2P Files) Data Set (.zip file)			
Case Style	DQ849 Plastic package, exposed paddle, lead finish: Matte-tin			
Tape & Reel Standard quantities available on reel	F104 7" reels with 10, 20, 50, 100, 200, 500, 1000 or 2000 devices.			
Suggested Layout for PCB Design	PL-328			
Evaluation Board	TB-547-2+			
Environmental Ratings	ENV08T1			

### **ESD Rating**

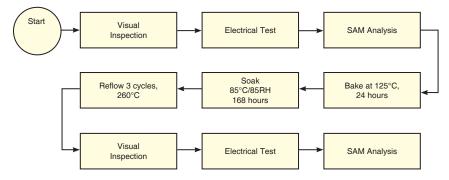
Human Body Model (HBM): 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001

Machine Model (MM): M1 (25V) in accordance with ANSI/ESD STM5.2-1999

#### **MSL Rating**

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

#### **MSL Test Flow Chart**



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