



Cypress Semiconductor Corporation, 198 Champion Court, San Jose, CA 95134. Tel: (408) 943-2600

## PRODUCT INFORMATION NOTIFICATION

**PIN:** PIN192201

**Date:** May 30, 2019

**Subject:** Discontinuation of HOLD Pin Operation on FM25V20A Products

**Change Type:** Minor

### Product Information:

This notification is to inform customers about a decision to remove the HOLD pin operation on Cypress FM25V20A products.

Cypress FM25V20A is a 2-Mbit nonvolatile memory employing an advanced high-reliability ferroelectric process (F-RAM). It supports 2.0V - 3.6V  $V_{DD}$  range and SPI interface up to a max frequency of 40MHz. It features a HOLD pin that can be used to interrupt a serial data communication operation without aborting it.

To comply with Cypress' corporate-wide SPI and Quad-SPI serial memory interface specification (refer to the attached datasheets of the recently released 4-Mbit SPI CY15x104QN product and the updated 2-Mbit SPI FM25V20A product), the HOLD pin functionality will not be supported in the FM25V20A SPI F-RAM products going forward. The intent behind this change is to ensure easy re-use of IP across products to achieve accelerated design cycles and common test programs. There is no change to Silicon or manufacturing sites.

In summary, we plan to remove the HOLD functionality going forward and designate the pin as Do Not Use (DNU).

The updated product datasheet is attached to this notification and can be downloaded from the Cypress Website ([www.cypress.com](http://www.cypress.com)).

### Part Numbers Affected: 8

See the attached 'Affected Parts List' file for a list of all part numbers affected by this change. Note that any new parts that are introduced after the publication of this PIN will include all changes outlined in this PIN.

### Approximate Implementation Date:

This change will be effective with the date of this notification. Revised datasheets are now live on [www.cypress.com](http://www.cypress.com). Please visit the corresponding product page to download the latest copy.

**Anticipated Impact:**

If the HOLD functionality is used in the customer's configuration, the customer's application may be affected by this change.

Cypress recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

**Method of Identification:**

Cypress maintains traceability of product to wafer level, including wafer fabrication location, through the lot number marked on the package.

**Response Required:**

No response is required.

For additional information regarding this change, contact your local sales representative or contact the PCN Administrator at [pcn\\_adm@cypress.com](mailto:pcn_adm@cypress.com).

Sincerely,

Cypress PCN Administration

<b>Item</b>	<b>Marketing Part Number</b>
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- |   |                |
|---|----------------|
| 1 | FM25V20A-DG    |
| 2 | FM25V20A-DGQ   |
| 3 | FM25V20A-DGQTR |
| 4 | FM25V20A-DGTR  |
| 5 | FM25V20A-G     |
| 6 | FM25V20A-GTR   |
| 7 | FM25V20A-PG    |
| 8 | FM25V20A-WAF   |