

PCN Number:	20190129002.0	PCN Date:	January 30, 2019
Title:	Datasheet for MSP430FR4131-MSP430FR4133		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



MSP430FR4133, MSP430FR4132, MSP430FR4131
SLAS865D – OCTOBER 2014 – REVISED JANUARY 2019

Changes from August 29, 2018 to January 17, 2019	Page
• Throughout the document, changed Modulation Oscillator (MODOSC) to Modulation Oscillator Clock (MODCLK) ...	1
• Added "or memory corruption" in table that starts "Stresses beyond those listed..." of Section 5.1, Absolute Maximum Ratings	14
• Added note of VLO clock frequency shift in LPM3 and LPM4 mode in Table 5-6, Internal Very-Low-Power Low-Frequency Oscillator (VLO)	23
• Changed from R_i to $R_{i,MUX}$ in Table 5-17, ADC, Power Supply and Input Range Conditions	32
• Added $R_{i,Misc}$ TYP value 34k Ω in Table 5-17, ADC, Power Supply and Input Range Conditions	32
• Removed ADCDIV from the formula because ADCCLK is after division in Table 5-18, ADC, 10-Bit Timing Parameters	32
• Added formula for R_i calculation in Table 5-18, ADC, 10-Bit Timing Parameters	32
• Remove description of " $\pm 3^\circ\text{C}$ " in table note that starts "The device descriptor structure ..." of Table 5-19, ADC, 10-Bit Linearity Parameters	33
• Add "10b" for ADCSSEL bit in Table 6-6, Clock Distribution	41
• Added "Clock Distribution Block Diagram" in Section 6.9.2, Clock System (CS) and Clock Distribution	41
• Corrected bitfield from IRDSEL to IRDSSEL in Section 6.9.8, Timers (Timer0_A3, Timer1_A3) , in the description that starts "The interconnection of Timer0_A3 and ..."	46
• Corrected the ADCINCHx column heading in Table 6-12, ADC Channel Connections	47
• Added word "Sensor" in Table 6-27, Device Descriptors	66
• Added word "Sensor" in Table 6-27, Device Descriptors	66

The datasheet number will be changing.

Device Family	Change From:	Change To:
MSP430FR4133, MSP430FR4132, MSP430FR4131	SLAS865C	SLAS865D

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/MSP430FR4131>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

MSP430FR4131IG48	MSP430FR4131IG48R	MSP430FR4131IG56	MSP430FR4131IG56R
MSP430FR4131IPMR	MSP430FR4132IG48	MSP430FR4132IG48R	MSP430FR4132IG56
MSP430FR4132IG56R	MSP430FR4132IPMR	MSP430FR4133IG48	MSP430FR4133IG48R
MSP430FR4133IG56	MSP430FR4133IG56R	MSP430FR4133IPM	MSP430FR4133IPMR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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