

PCN Number:	20151110001	PCN Date:	2/20/2016
Title:	Datasheet for ADS41B29/ ADS41B49		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
		<input type="checkbox"/>	Wafer Bump Site
		<input type="checkbox"/>	Wafer Bump Material
		<input type="checkbox"/>	Wafer Bump Process
		<input type="checkbox"/>	Wafer Fab Site
		<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.



ADS41B29, ADS41B49

SBAS486F –NOVEMBER 2009–REVISED FEBRUARY 2016

Changes from Revision E (July 2012) to Revision F	Page
• Changed title and changed ADS41B49/29 to ADS41Bx9 and QFN to VQFN throughout document	1
• Added Applications section, Device Information table, front-page figure, ESD Ratings table, Feature Description section, Device Functional Modes section, Programming section, Register Maps section, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section	1
• Deleted Ordering Information table	1
• Changed Pin Functions table: changed title and format	6
• Added Added last row to Voltage applied to input pins section in Absolute Maximum Ratings table	7
• Changed Temperature parameters in Absolute Maximum Ratings table: changed format of Temperature section and changed maximum specifications for T _A and T _J	7
• Changed TYP column header to NOM in Recommended Operating Conditions table	8
• Changed Digital Outputs, T _J parameter in Recommended Operating Conditions table	8
• Deleted High-Performance Modes section from Recommended Operating Conditions table	8
• Changed conditions of Electrical Characteristics: General table from temperature to ambient temperature	9
• Changed conditions of Electrical Characteristics: ADS41B29, ADS41B49 table from temperature to ambient temperature	10
• Added footnote 1 to Electrical Characteristics: ADS41B29, ADS41B49 table	10
• Changed conditions of Timing Requirements: LVDS and CMOS Modes table from temperature to ambient temperature	12
• Added footnotes 6 and 7 to Timing Requirements: LVDS and CMOS Modes table	12
• Added footnote 1 to Timing Requirements: Reset table	13
• Changed title of Figure 13 and Figure 14	16
• Changed title of Figure 31 and Figure 32	19
• Changed conditions of Table 6	38
• Added Summary of High-Performance Modes section	40
• Changed bit registers to satisfy new standard requirements	41

The datasheet number will be changing.

Device Family	Change From:	Change To:
ADS41B29/ADS41B49	SBAS486E	SBAS486F

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/ADS41B49>

Reason for Change:			
To more accurately reflect device characteristics.			
Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):			
No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.			
Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
ADS41B29IRGZ25	ADS41B29IRGZR	ADS41B29IRGZT	ADS41B49IRGZR
ADS41B49IRGZT			

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com