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Bulletin Date:	4/15/2014		Bulletin Effective Date:	4/15/2014	
Title:	Clock Builder Desktop Software New Revision				
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Bulletin Details					
Description:					
Silicon Labs is pleased to announce the latest version of the Clock Builder Desktop Software v6.3 featuring a number of updates enhancing the user experience.					
The latest version of the Clock Builder Desktop Software has the following updates:-					
<ol style="list-style-type: none"> 1. Clock outputs can now be disabled at startup. 2. Software can be used to automatically generate custom part numbers. 3. “Zero Delay Buffer (ZDB) Mode” option added to the “Frequency Plan” tab. Selecting this option automatically configures clock inputs and outputs to enable ZDB mode. 4. “Output Frequency” entry boxes have been added for each clock output. 5. Reference Clock (PLL Bypass) option has been added to each clock output. 					
If you have questions about this Product Bulletin, please contact your Silicon Labs sales representatives. A list of Silicon Labs sales representatives is available at www.silabs.com					
Reason:					
The software has been updated to enhance the overall user experience; it is easier for the user to select options such as ZDB mode and PLL Bypass. Additionally, the software can now be used to generate custom part numbers automatically.					
Product Identification:					
Si5334, Si5335, Si5338, Si5356					
Customer Actions Needed:					
Go to www.silabs.com/custom-timing to download latest version of the software.					