# Product/Process Change Notice - PCN 19_0190 Rev. -

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This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

**PCN Title:** LTC3897 Datasheet Electrical Specification Change

**Publication Date:** 11-Sep-2019

**Effectivity Date:** 14-Dec-2019 *(the earliest date that a customer could expect to receive changed material)*

**Revision Description:**
Initial Release

**Description Of Change:**

Burst Mode at Run=DGEN=12V, SGEN=0V, VFB=1.25V(No Load), CS=12V, IS+=IS-=CS-0.1V. The Maximum Specified Limit will change from 175uA to 190uA.

Burst Mode at Run=12V, DGEN=0V, SGEN=12V, VFB=1.25V(No Load), CS=IS+=IS-=12V. The Maximum Specified Limit will change from 350uA to 380uA.

Top Gate Off to Bottom Gate on Switch-On Delay Time when DTC=0V. The Maximum Specified Limit will change from 70nS to 75nS.

Top Gate Off to Bottom Gate on Switch-On Delay Time when DTC=Float. The Maximum Specified Limit will change from 120nS to 130nS.

Top Gate Off to Bottom Gate on Switch-On Delay Time when DTC=INTVcc. The Maximum Specified Limit will change from 235nS to 275nS.

Bottom Gate Off to Top Gate On Switch-On Delay Time when DTC=0V. The Maximum Spcified Limit will change from 70nS to 75nS.

Bottom Gate Off to Top Gate On Switch-On Delay Time when DTC=Float. The Maximum Specified Limit will change from 120nS to 130nS.

Bottom Gate Off to Top Gate On Switch-On Delay Time when DTC=INTVcc. The Maximum Specified Limit will change from 235nS to 275nS.

SG Pin Output High Voltage(Vsg-Vcs) condition change from (Vin=8V to 75V, Isg=0,-1uA) to (Vin=8V to 70V, Isg=0, -1uA).

Overcurrent Faul Threshold, (Vis+ - Vis-) condition change from IS-<1.5V to IS-=1.5V.

TMR Pin Pull-Up Current, Overvoltage when TMR=1V, SPFB=1.5V, Vin-Vis-=0.5V. The Maximum Speficied Limit will change from -3.2uA to -3.7uA. The Typical Specified Limit will change from -2.3uA to -2.5uA.

TMR Pin Pull-Up Current, Overcurrent when TMR=1V, Delta Vis=60mV, Vin-Vis-=0.5V. The Maximum Specified Limit will change from -15uA to -16uA.

TMR Pin Pull-Up Current condition will change from (TMR=1V, Delta Vis=60mV, Vin-Vis-=75V) to (TMR=1V, Delta Vis=60mV, Vin-Vis=70V).

TMR Pin Pull-Up Current when TMR=1V, Delta Vis=60mV, Vin-Vis-=70V. The Minimum Specified Limit will change from -230uA to -210uA. The Typical Specified Limit will change from -270uA to -250uA. The Maximum Specified limit will change from -310uA to -290uA.

TMR Pin Pull-up Current, Retry when TMR=1V, SPFB=1.5V. The Typical Specified Limit will change from -2.3uA to -2.5uA. The Maximum Specifie Limit will change from -3.2uA to -3.7uA.

Retry Duty Cycle, Overcurrent when Delta Vis=60mV, Vin-Vis-=12V. The Maximum Specified Limit will change from 0.11% to 0.12%.

TMR Pin Thresholds condition will change from (SG Falling, Vin=4.2V to 75V) to (SG Falling, VIN=4.2V to 70V).

TMR Pin Thresholds condition will change from (SG Rising (after 32 cycles), Vin=4.2V to 75V) to (SG Rising (after 32 cycles), Vin=4.2V to 70V).

DG Pin Output High Voltage, (Vdg-Vcs) condition will change from (8V<Vin<75V, Idg=0,-1uA,No Fault, SG Open) to (8V<Vin<70V, Idg=0,-1uA,No Fault, SG Open)

Source-Drain Regulation Voltage, (Vcs-Vis+) condition will change from (DG-CS=2.5V, Vin=CS=4.2V to 75V) to (DG-CS=2.5V, Vin=CS=4.2V to 70V)

**Reason For Change:**

To accurately reflect device capabilities.

**Impact of the change (positive or negative) on fit, form, function & reliability:**

No change to Product Design. This limit change will have no impact on the form, fit, function, quality or reliability of the device.

**Product Identification** *(this section will describe how to identify the changed material)*

The product shipped after effectivity date will be tested to the new limits.

**Summary of Supporting Information:**

Changes reflected on the attached amended Product Datasheet revision. Rever to Electrical Characterisistcs Table on Page3, 4, 5 and 6.

**Supporting Documents**

**Attachment 1: Type**: Datasheet Specification Comparison

ADI_PCN_19_0190_Rev_-_LTC3897 Datasheet Edits.pdf

| For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives. | | | |
|---|---|---|---|
| **Americas:**<br>PCN_Americas@analog.com | **Europe:**<br>PCN_Europe@analog.com | **Japan:**<br>PCN_Japan@analog.com | **Rest of Asia:**<br>PCN_ROA@analog.com |

| Appendix A - Affected ADI Models | | | | |
|---|---|---|---|---|
| **Added Parts On This Revision - Product Family / Model Number (12)** | | | | |
| LTC3897 / LTC3897EFE#PBF | LTC3897 / LTC3897EFE#TRPBF | LTC3897 / LTC3897EUHF#PBF | LTC3897 / LTC3897EUHF#TRPBF | LTC3897 / LTC3897HFE#PBF |
| LTC3897 / LTC3897HFE#TRPBF | LTC3897 / LTC3897HUHF#PBF | LTC3897 / LTC3897HUHF#TRPBF | LTC3897 / LTC3897IFE#PBF | LTC3897 / LTC3897IFE#TRPBF |
| LTC3897 / LTC3897IUHF#PBF | LTC3897 / LTC3897IUHF#TRPBF | | | |

| Appendix B - Revision History | | | |
|---|---|---|---|
| **Rev** | **Publish Date** | **Effectivity Date** | **Rev Description** |
| Rev. - | 11-Sep-2019 | 14-Dec-2019 | Initial Release |
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